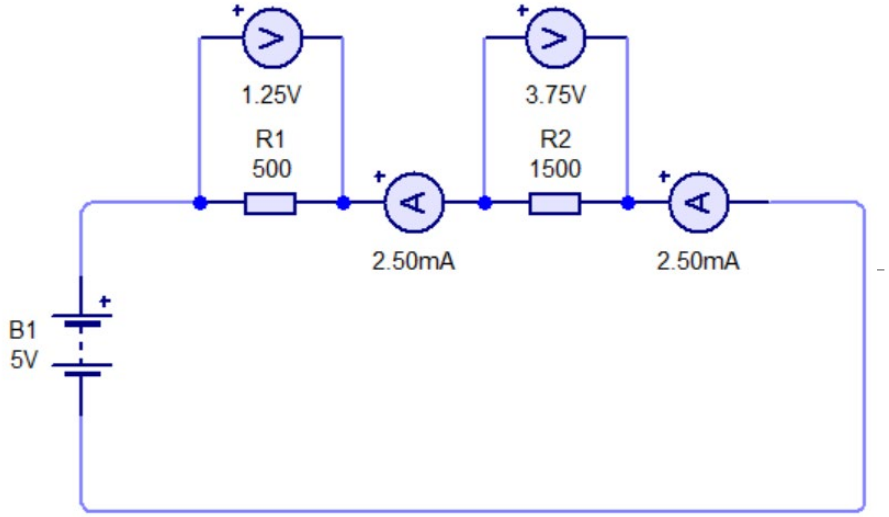
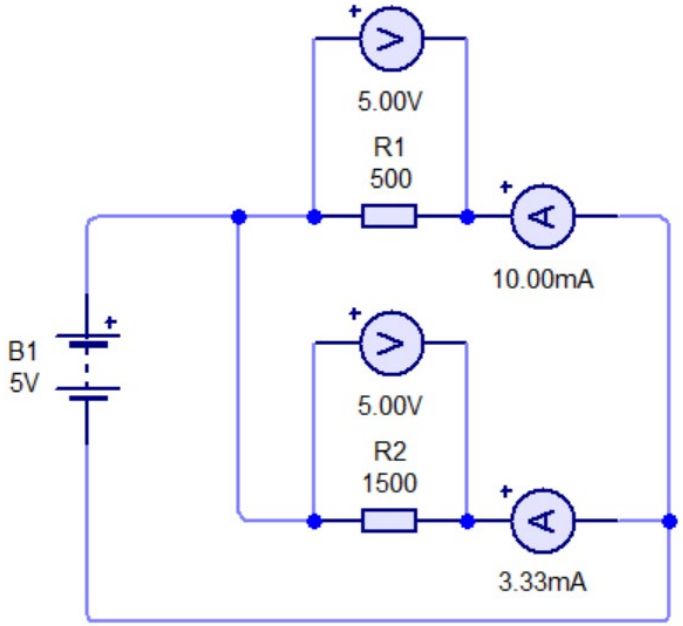
Kirchoff’s Laws

Recap on DC Circuits

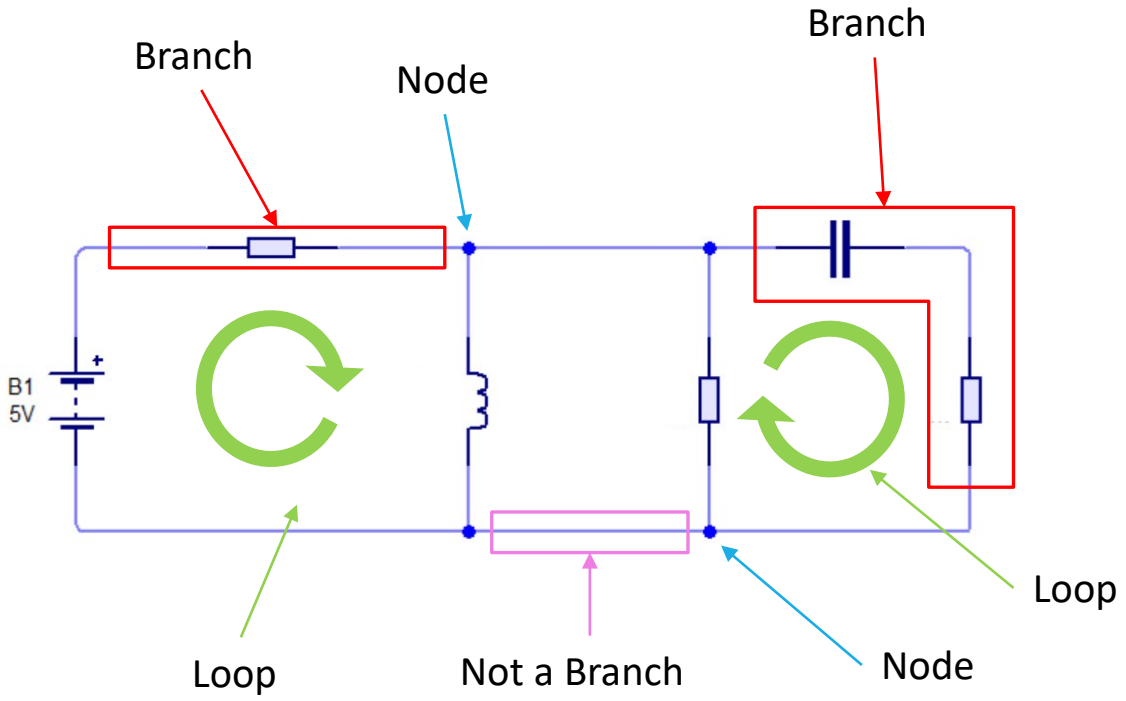
- Ohms law (V=IR)
- **Series connection:** Current is the same through all components, voltages split
- **Parallel connection:** Voltage is the same across branches, currents split





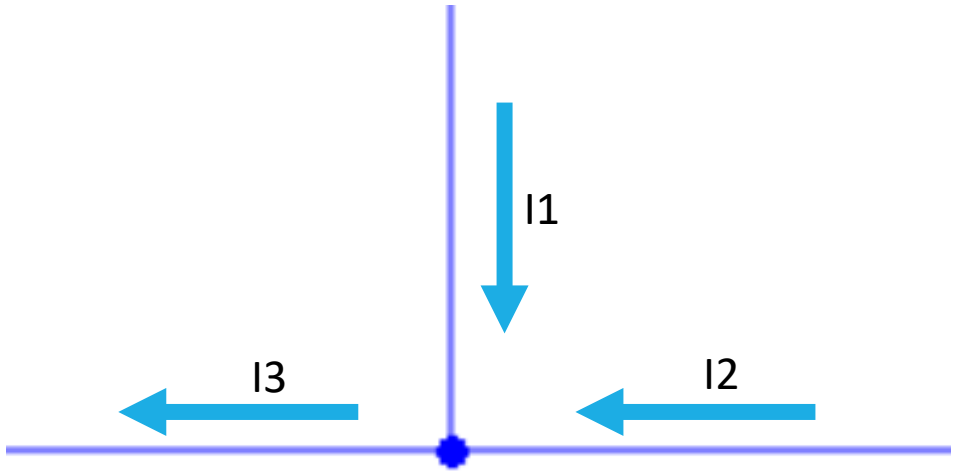
Parts of DC Circuits

- **Node**: A point in a circuit where two or more components are connected.
- **Branch**: A single path connecting two nodes that contains a circuit element (e.g., resistor, voltage source).
- **Loop/Mesh**: Any closed conducting path in a circuit where you can start at one point, travel through elements, and return to the starting point without retracing.



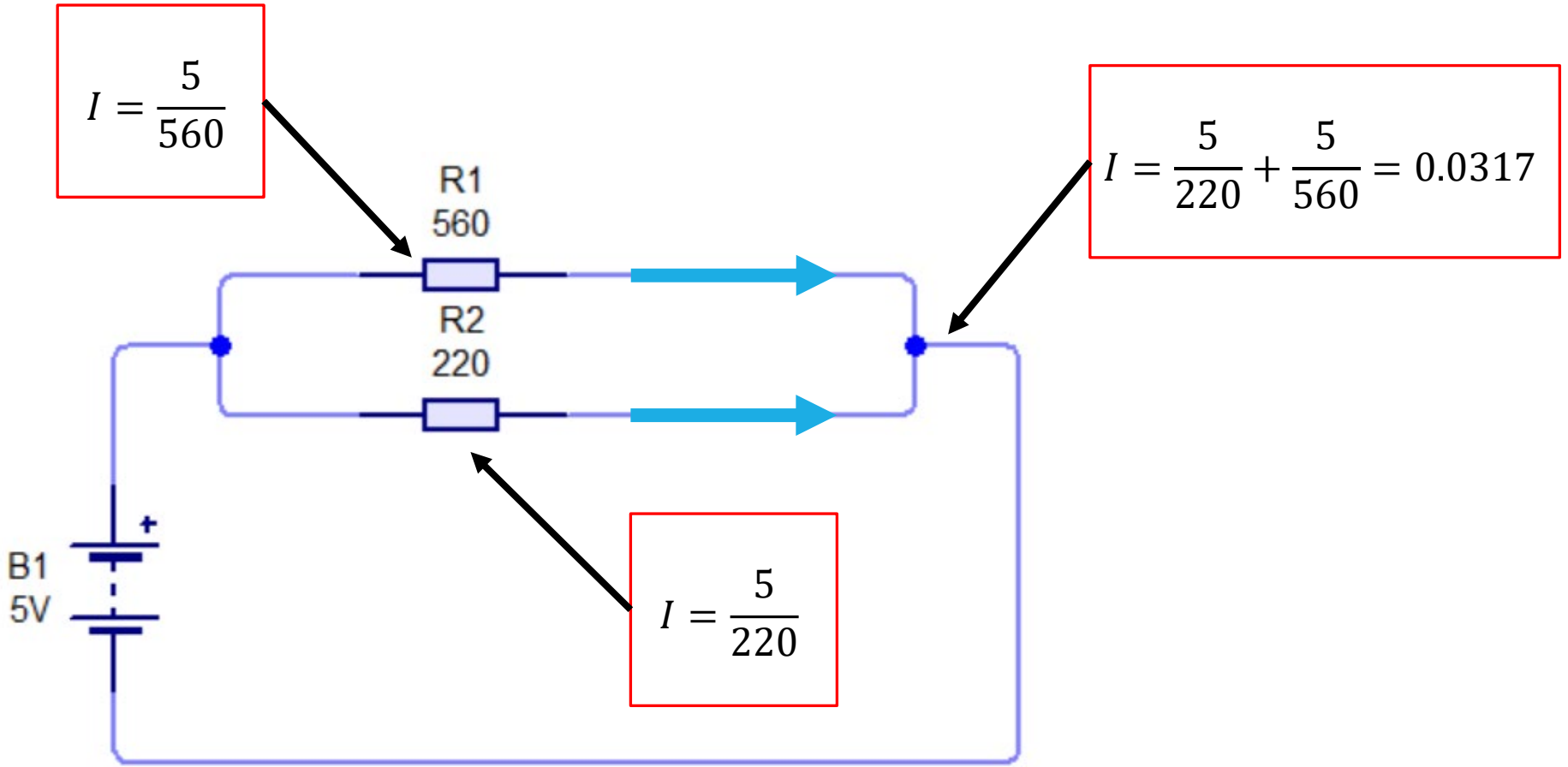
Kirchoff's Current Law

- **Definition:** At any junction, the total current entering = total current leaving
- **Equation form:** $\sum I_{in} = \sum I_{out}$
- **Basis:** Conservation of charge



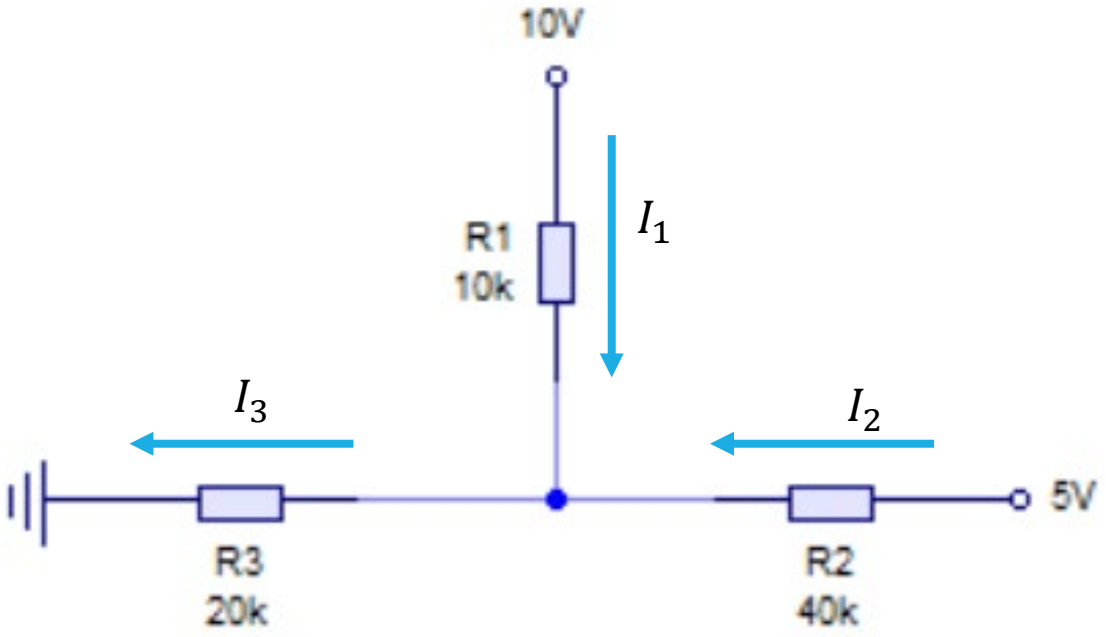
$$I1 + I2 = I3$$

KCL Example


$I = \frac{5}{560}$
$I = \frac{5}{220} + \frac{5}{560} = 0.0317$
R1
560
R2
220
B1
5V
+
$I = \frac{5}{220}$

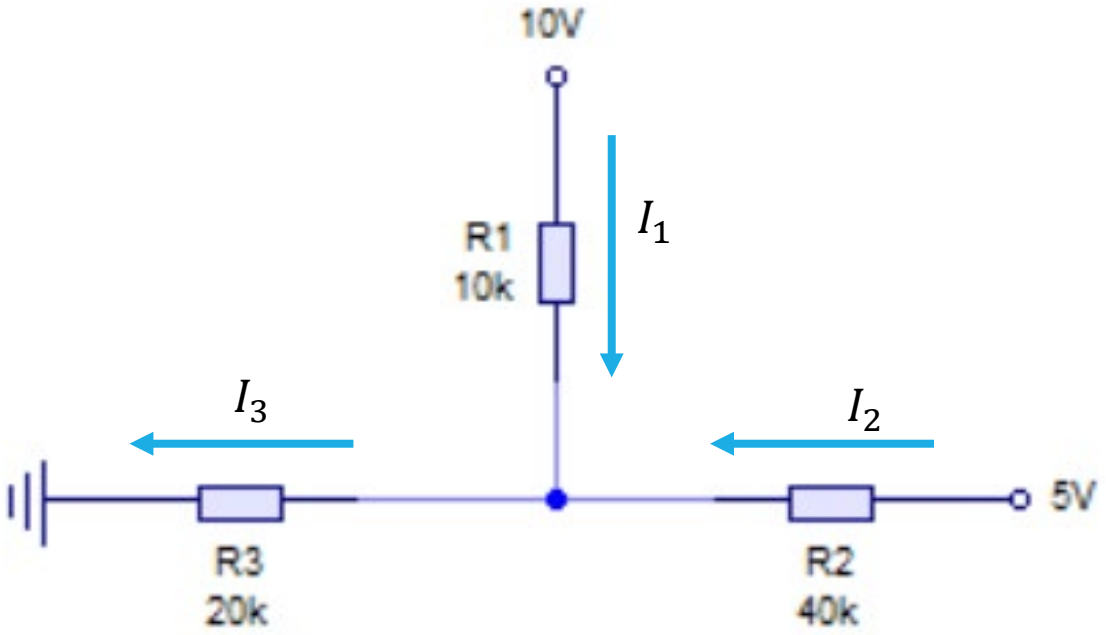
KCL Example 2

- With this example we need to work out the voltage at the node using KCL
- The only thing we must consider is the Voltage at the node in our calculation
- $\frac{V_N - V_1}{R_1} + \frac{V_N - V_2}{R_2} + \frac{V_N}{R_3} = 0$
- $\frac{V_N - 10}{10000} + \frac{V_N - 5}{40000} + \frac{V_N}{20000} = 0$



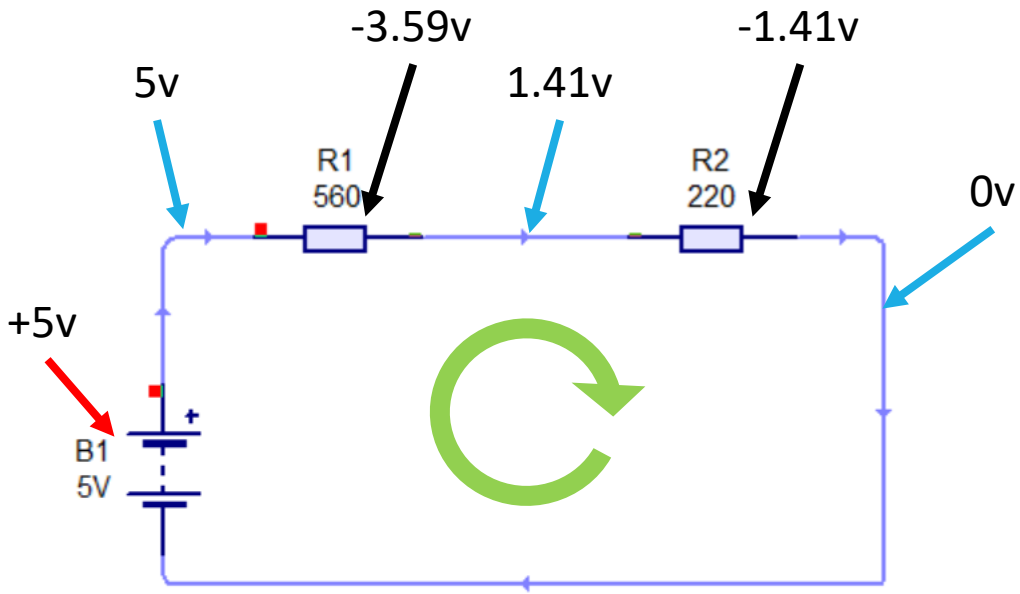
KCL Example 2

- $\frac{V_N-10}{10000}+\frac{V_N-5}{40000}+\frac{V_N}{20000}=0$
- We then clear the denominators by x40000
- $4(V_N-10)+(V_N-5)+2(V_N)=0$
- $4V_N-40+V_N-5+2V_N=0$
- $7V_N-45=0$
- $V_N=6.428571429$



Kirchoff's Voltage Law

- **Definition:** The sum of all voltages around any closed loop in a circuit is zero.
- **Equation form:** $\sum V = 0$
- **Meaning:** Energy is conserved—voltage rises (sources) are balanced by voltage drops (loads).
- **Rule of thumb:** When you go around a loop, add rises as positive, drops as negative.

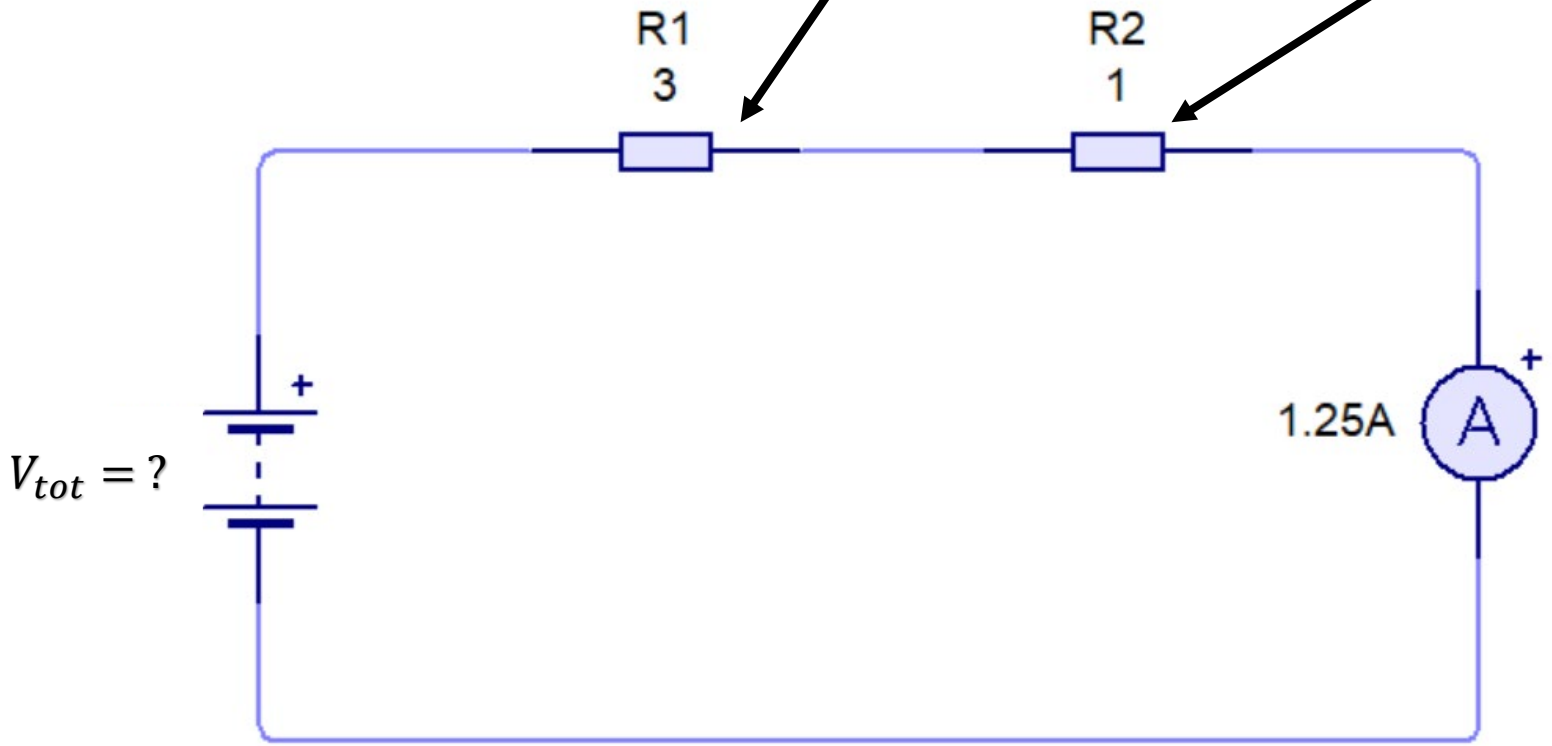


$$5v - 3.59v - 1.41v = 0v$$

KVL Example

$V_1 = 1.25 * 3 = 3.75$

$V_2 = 1.25 * 1 = 1.25$

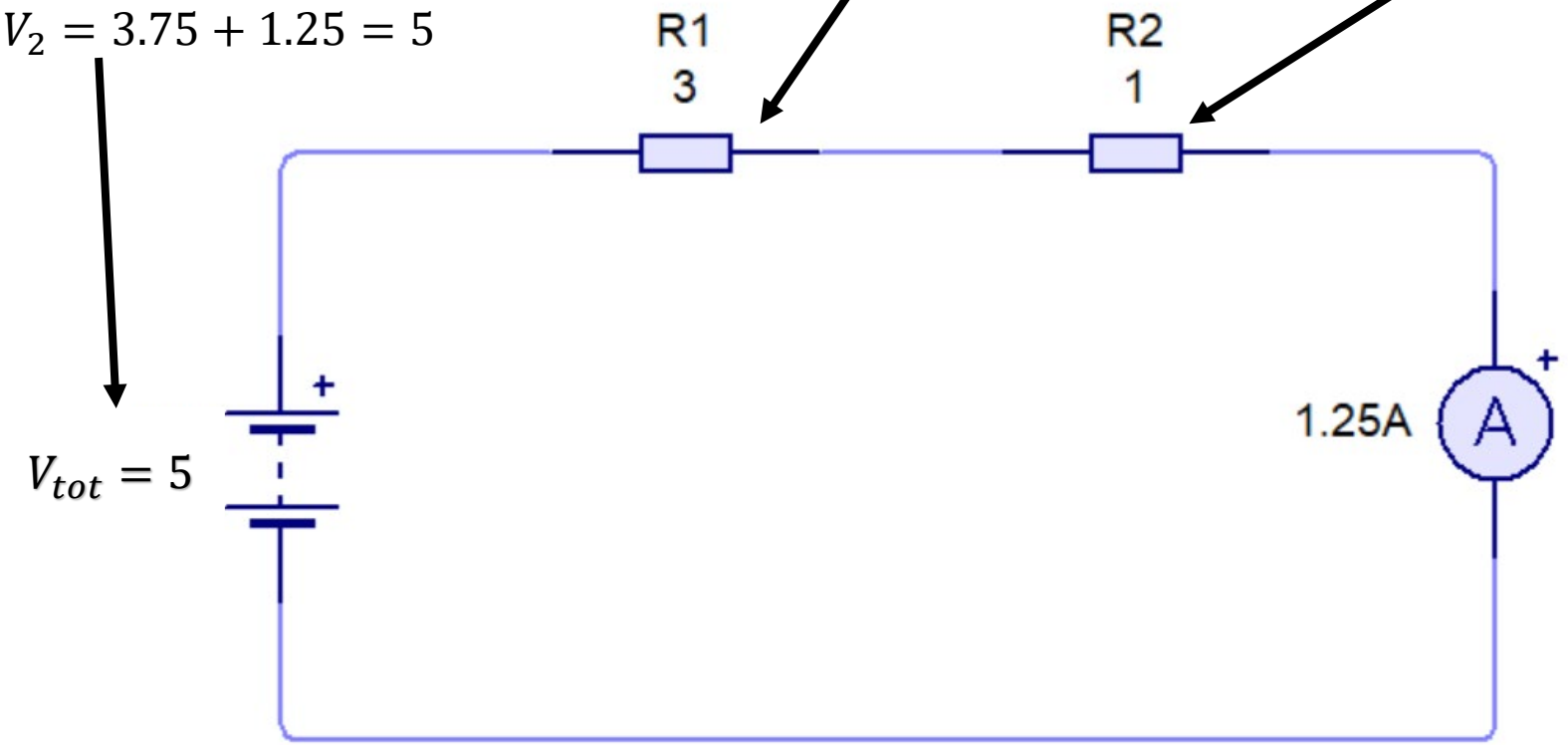


KVL Example

$V_1 = 1.25 * 3 = 3.75$

$V_2 = 1.25 * 1 = 1.25$

$V_{tot} = V_1 + V_2 = 3.75 + 1.25 = 5$



$V_{tot} = 5$

Mesh/Loop Analysis

- We mainly use Kirchoff's Laws in Mesh Analysis
- Mesh Analysis allows us to look at voltage and current values all around the circuit
- We do this by dividing our circuit into "meshes" or loops and then doing KCL and KVL analysis